

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S20 9	183	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/08 16:42
S21 0	7	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver and (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same differential with signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 06:31
S21 1	163	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver or (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same differential with signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:59
S21 2	82	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver or (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same (input and output) same differential with signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:58
S21 3	1	10/090,250	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:19
S21 5	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver or (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same (input and output) same differential with signal and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:58
S21 6	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver or (receiv\$3 or (driver or transmi\$5 or sender)) or modem) same differential with signal and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:00
S21 7	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (transceiver or (receiv\$3 or (driver or transmi\$5 or sender)) or modem) and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:00
S21 8	0	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configuration adj3 memory adj3 cell and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:02
S21 9	0	configuration adj3 memory adj3 cell and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:17

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S22 0	10545	(loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:01
S22 1	3733	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:03
S22 2	2	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and configura\$4 with memory adj3 cell and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:02
S22 3	692	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and (loss with (sync or synchroni\$6)) with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:44
S22 4	33	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and (loss with (sync or synchroni\$6)) with detect\$3 same transceiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:03
S22 5	112	(IC or (integrated adj1 circuit) or PLD or PLA or ASIC or DSP or UART or chip) and (loss with (sync or synchroni\$6)) adj2 detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:44
S22 6	1251	configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:19
S22 8	0	S220 and S226	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:15
S22 9	4	configura\$4 with memory adj3 cell and (loss with (sync or synchroni\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:17
S23 0	18	configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver) same (input and output) same differential with signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:31

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S23 1	2	configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver) same CRC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 09:32
S23 2	25	configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem or driver) and CRC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 08:59
S23 3	230	configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:00
S23 4	0	LSI same configuration adj1 memory adj1 cell and (transceiver or receiver or transmitter or modem)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:00
S23 5	1197	synchronization near (benefit or advantage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:01
S23 6	8	transceiver same synchronization near (benefit or advantage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:03
S23 7	62	transceiver and synchronization near (benefit or advantage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:06
S23 8	0	transceiver and (benefit or advantage) adj1 of adj1 synchronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:06
S23 9	56	transceiver and (benefit or advantage) adj1 synchronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/10 09:06